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Results 1 - 10 of about 124,000 for **cache miss frequency** . (0.05 seconds)

[PPT] www.hpcaconf.org/hpca11/slides/Predicting.ppt

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Given a circular sequence $cseqX(dX, nX)$, the last access is a **cache miss** iff $dX >$

Assoc ... Not vulnerable to interfering thread's **miss frequency** ...

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Cache miss. rates indicate the **frequency** of memory accesses and enable us to ...

Since the execution time varies with **frequency**, **cache misses** over clock ...

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[PPT] [FAST : Frequency-Aware Static Timing Analysis](#)

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Ignores effect of **frequency** scaling on memory accesses ... Stall due to I-cache

miss is shown. Model accurately captures memory latency, however long ...

moss.csc.ncsu.edu/~mueller/seminar/fall03/rtss03.ppt - [Similar pages](#)

[PPT] moss.csc.ncsu.edu/~mueller/seminar/fast_poster.ppt

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use of **caches** with the **frequency** model is optional. ... Example 3: Capturing effect of I-cache and D-cache **miss** ($WCEC = 9 + 2N$). Results ...

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[PS] [Cache-Memory and TLB Calibration Tool Stefan Manegold 1 Peter ...](#)

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The **frequency** of **cache misses** depends on the access stride and the **cache** line size.

... Matching curves mean, that the **cache miss frequency** has reached its ...

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